Panel
Session

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SLIDE 1

What are technology challenges for future memory systems

➡ programmability, persistence, performance

What are architectural challenges for future memory systems

➡ how to integrate the emerging memory technologies

What are memory solutions for data centric HPC computing?

➡ nonvolatiles, RDMA, device-level protocols … network?

What are prospects for processing in/near memory?

➡ good question

What are the roles of persistent memory for HPC?

➡ it's cheap, it's fast (now), and so you're going to have it one way or the other, so deal with it :D