

Proceedings of

**ESPM2 2016: Second International
Workshop on
Extreme Scale Programming Models and
Middleware**



Held in conjunction with

**SC16: The International Conference
for High Performance Computing,
Networking, Storage and Analysis**

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2016 Second International Workshop on Extreme Scale Programming Models and Middleware

ESPM2 2016

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Message from the Program Chairs

Welcome to ESPM2 '16 workshop! As the HPC field is heading to Exascale, the role of Programming Models and Middleware is getting more important. The objectives of this workshop are to bring together researchers working in this area and discuss the state-of-the-art developments in the field.

The detailed workshop program is indicated in the previous page. We would like to thank all authors who submitted papers to this workshop. Special thanks go to the program committee members for providing us with high-quality reviews under tight deadlines. For each submitted paper, we were able to collect at least three reviews. We were able to receive 100% reviews on a tight deadline. Based on the reviews and on-line discussion among the PC members, a set of five regular papers and two short papers were selected. These papers reflect the state-of-the-art research and developments being conducted in the community in the emerging programming models and middleware area.

We are extremely thankful to our Keynote Speaker (Prof. Thomas Sterling). We thank Intel Corp for generously sponsoring the best paper award. Our special thanks to SigHPC for publishing the proceedings of the workshop. It has been a pleasure to work with Ewa Deelman and Maciej Malawski (SC '16 Workshop Chairs) to get this workshop organized. Last but not the least, our sincere thanks are due to the attendees, without whom this conference would not be a success. We hope you will enjoy the program!!

Khaled Hamidouche, Karl W. Schulz, Hari Subramoni and Dhableswar K. (DK) Panda
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